

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A multi-chip module system comprising:  
a substrate having at least a first position having, in turn, a predetermined configuration for locating a first semiconductor device thereat and having at least one other vacant position having, in turn, a predetermined configuration for locating a second semiconductor device thereat on the multi-chip module system; and  
a first semiconductor device located in the at least first position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; said first semiconductor device ~~being~~ has been burned in at said ~~location~~ at least first position on said substrate.

2. (Previously Presented) The multi-chip module system of claim 1, further comprising:  
the at least one other vacant position having the predetermined configuration for locating the second semiconductor device thereat which is substantially the same as the predetermined configuration of the first position.

3. (Previously Presented) The multi-chip module system of claim 1, further comprising:  
the at least one other vacant position having the predetermined configuration for locating the second semiconductor device thereat; and  
the second semiconductor device having a predetermined performance characteristic substantially similar to that of the first predetermined performance characteristic of the first semiconductor device.

4. (Previously Presented) The multi-chip module system of claim 1, further comprising:  
the at least one other vacant position having the predetermined configuration for locating the second semiconductor device thereat; and  
the second semiconductor device having a second predetermined performance characteristic of at least substantially twice that of the first predetermined performance characteristic of the first semiconductor device.

5 (Currently Amended) A multi-chip module system comprising:  
a substrate having a first position having, in turn, a predetermined configuration for locating a first semiconductor device thereat, having a second position having, in turn, a predetermined configuration for locating a second semiconductor device thereat, and having at least one other vacant position having, in turn, a predetermined configuration for locating a third semiconductor device thereat on the multi-chip module system;  
the first semiconductor device located in the first position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; and  
the second semiconductor device located in the second position of the substrate for use in the multi-chip module system, the second semiconductor device having a second predetermined performance characteristic; said first and second semiconductor devices ~~being~~ have been burned in at said first and second positions, respectively, on said substrate.

6. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
the at least one other vacant position having a predetermined configuration for locating a third semiconductor device thereat which is substantially the same as the predetermined configuration of the first position.

7. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
the at least one other vacant position having a predetermined configuration for locating a third semiconductor device thereat; and  
the third semiconductor device having a predetermined performance characteristic substantially similar to that of the first predetermined performance characteristic of the first semiconductor device.

8. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
the at least one other vacant position having a predetermined configuration for locating a third semiconductor device thereat; and  
the third semiconductor device having a predetermined performance characteristic of at least substantially twice that of the first predetermined performance characteristic of the first semiconductor device.

9. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
the at least one other vacant position having a predetermined configuration for locating a third semiconductor device thereat; and  
the third semiconductor device having a predetermined performance characteristic of at least substantially three times greater than that of the second predetermined performance characteristic of the second semiconductor device.

10. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
the at least one other vacant position having a predetermined configuration for locating a third semiconductor device thereat; and

the third semiconductor device having a predetermined performance characteristic of at least substantially four times greater than that of the first and the second predetermined performance characteristic of the first semiconductor device and the second semiconductor device combined.

11. (Currently Amended) The multi-chip module system of claim-4 5, wherein the first semiconductor device comprises a memory device.

12. (Currently Amended) The multi-chip module system of claim-4 5, wherein the second semiconductor device comprises a memory device.

13. (Currently Amended) The multi-chip module system of claim-4 5, wherein the first semiconductor device comprises a microprocessor device.

14. (Currently Amended) The multi-chip module system of claim-4 5, wherein the second semiconductor device comprises a microprocessor device.

15. (Currently Amended) The multi-chip module system of claim-4 5, wherein the multi-chip module system comprises a single in-line memory module system.

16. (Currently Amended) The multi-chip module system of claim-4 5, further comprising:  
a third semiconductor device; and  
an adapter connected to the third semiconductor device, the adapter having a configuration for connecting the adapter to the at least one other vacant position on the substrate to allow connection of the third semiconductor device to the substrate.

17. (Previously Presented) A multi-chip module system comprising:  
a substrate having two opposing sides, said substrate having a first position having, in turn, a predetermined configuration for locating a first semiconductor device thereat, having a second position having, in turn, a predetermined configuration for locating a second semiconductor device thereat, having a first vacant position having, in turn, a predetermined configuration for locating a third semiconductor device thereat, and having a second vacant position having, in turn, a predetermined configuration for locating a fourth semiconductor device thereat on the multi-chip module system;  
the first semiconductor device located in the first position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; and  
the second semiconductor device located in the second position of the substrate for use in the multi-chip module system, the second semiconductor device having a second predetermined performance characteristic; said first and second semiconductor devices being burned in at said first and second positions, respectively, on said substrate.

18. (Currently Amended) The multi-chip system module of claim 17, wherein:  
the first vacant position and the second vacant position are on opposing sides ~~is on the side~~ of the substrate ~~which is opposite the side upon which the second vacant position is located.~~

19. (Previously Presented) A multi-chip module system comprising:  
a substrate having at least a first predetermined configuration position for locating a first semiconductor device thereat and having at least one other vacant predetermined configuration position for locating a second semiconductor device thereat on the multi-chip module system; and  
the first semiconductor device located in the at least the first predetermined configuration position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; said first semiconductor

device being burned in at said first predetermined configuration position on said substrate.

20. (Previously Presented) The multi-chip module system of claim 19, further comprising:  
the at least one other vacant predetermined configuration position for locating the second semiconductor device thereat which is substantially the same as the predetermined configuration of the first position.

21. (Previously Presented) The multi-chip module system of claim 19, further comprising:  
the at least one other vacant predetermined configuration position having a predetermined configuration for locating the second semiconductor device thereat; and  
the second semiconductor device having a predetermined performance characteristic substantially similar to that of the first predetermined performance characteristic of the first semiconductor device.

22. (Previously Presented) The multi-chip module system of claim 19, further comprising:  
the at least one other vacant predetermined configuration position having a predetermined configuration for locating the second semiconductor device thereat; and  
the second semiconductor device having a predetermined performance characteristic of at least substantially twice that of the first predetermined performance characteristic of the first semiconductor device.

23. (Previously Presented) A multi-chip module system comprising:  
a substrate having a first predetermined configuration position for locating a first semiconductor device thereat, having a second predetermined configuration position for locating a second semiconductor device thereat, and having at least one other vacant predetermined

configuration position for locating a third semiconductor device thereat on the multi-chip module system;

the first semiconductor device located in the first predetermined configuration position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; and

the second semiconductor device located in the second predetermined configuration position of the substrate for use in the multi-chip module system, the second semiconductor device having a second predetermined performance characteristic; wherein said first and second semiconductor devices being burned in at said first and second predetermined configuration positions, respectively, on said substrate.

24. (Previously Presented) The multi-chip module system of claim 23, further comprising:

the at least one other vacant predetermined configuration position for locating the third semiconductor device thereat which is substantially the same as the predetermined configuration of the first position.

25. (Previously Presented) The multi-chip module system of claim 23, further comprising:

the at least one other vacant predetermined configuration position for locating the third semiconductor device thereat; and  
the third semiconductor device having a third predetermined performance characteristic substantially similar to that of the first predetermined performance characteristic of the first semiconductor device.

26. (Previously Presented) The multi-chip module system of claim 23, further comprising:

the at least one other vacant predetermined configuration position for locating the third semiconductor device thereat; and

the third semiconductor device having a third predetermined performance characteristic of at least substantially twice that of the first predetermined performance characteristic of the first semiconductor device.

27. (Previously Presented) The multi-chip module system of claim 23, further comprising:  
the at least one other vacant predetermined configuration position for locating the third semiconductor device thereat; and  
the third semiconductor device having a third predetermined performance characteristic of at least substantially three times greater than that of the second predetermined performance characteristic of the second semiconductor device.

28. (Previously Presented) The multi-chip module system of claim 23, further comprising:  
the at least one other vacant predetermined configuration position for locating a third semiconductor device thereat; and  
the third semiconductor device having a third predetermined performance characteristic of at least substantially four times greater than that of the first and second predetermined performance characteristic of the first semiconductor device and the second semiconductor device combined.

29. (Original) The multi-chip module system of claim 23, wherein the first semiconductor device comprises a memory device.

30. (Original) The multi-chip module system of claim 23, wherein the second semiconductor device comprises a memory device.

31. (Original) The multi-chip module system of claim 23, wherein the first semiconductor device comprises a microprocessor device.



32. (Original) The multi-chip module system of claim 23, wherein the second semiconductor device comprises a microprocessor device.

33. (Original) The multi-chip module system of claim 23, wherein the multi-chip module system comprises a single in-line memory module system.

34. (Currently Amended) The multi-chip module system of claim 23, further comprising:  
an adapter connected to the third semiconductor device, the adapter having a configuration for connecting the adapter to the at least one other vacant predetermined configuration position on the substrate to allow connection of the third semiconductor device to the substrate.

35. (Currently Amended) A multi-chip module system comprising:  
a substrate having two opposing sides, said substrate having a first predetermined configuration position for locating a first semiconductor device thereat, having a second predetermined configuration position for locating a second semiconductor device thereat, having a first vacant predetermined configuration position for locating a third semiconductor device thereat, and having a second vacant predetermined configuration for locating a fourth semiconductor device thereat on the multi-chip module system;  
the first semiconductor device located in the first predetermined configuration position of the substrate for use in the multi-chip module system, the first semiconductor device having a first predetermined performance characteristic; and  
the second semiconductor device located in the second predetermined configuration position of the substrate for use in the multi-chip module system, the second semiconductor device having a second predetermined performance characteristic; said first and second semiconductor devices ~~being~~ have been burned in at said first and second predetermined configuration positions, respectively, on said substrate.

36. (Currently Amended) The multi-chip system module of claim 35, wherein:  
the first vacant predetermined configuration position and the second vacant  
predetermined configuration position are is located on opposing sides ~~the side of the~~  
substrate ~~which is opposite the side upon which the second vacant predetermined  
configuration position is located.~~